



COM-IT-CON

IEEE INTERNATIONAL CONFERENCE ON MACHINE LEARNING, BIG DATA, CLOUD AND PARALLEL COMPUTING: Trends, Perspectives and Prospects

26-27th, May, 2022



About COM-IT-CON,2022

The COM-IT-CON,2022 is a gathering of the Computer Science and Engineering department's technology forum at MRIIRS. The cornerstones of Industry 4.0 are Artificial Intelligence, Machine Learning, Big Data, Cloud Computing, and Parallel Computing. The Forum intends to stimulate and promote research in Machine Learning, Big Data, Cloud, and Parallel Computing. They also allow for the use of pillars like the Internet of Things (IoT), Augmented Reality, Cyber Security, Cloud Computing, and finally 5G Networks. The conference emphasizes these focus areas but is also open for original submissions in other emerging trends, recent advances, cutting-edge innovations, and applications in these aforementioned pillars.

Conference Objectives

In today's fast-paced technology era, the conference aims to present a forum for presenting, brainstorming, and exchanging the digital, physical, and biological worlds as they grow increasingly entangled. The Com-IT-Con 2022 conference's main purpose is to bring together academics, researchers, and industry leaders from around the world to discuss and debate the most recent advancements in computing disciplines like Machine Learning, Big Data, Cloud Computing, and Parallel Computing. The conference aims to establish scientifically the links between research teams and experts from other nations to facilitate future collaborations.

Core Committee

- Chief Patron:** Mrs. Satya Bhalla, Chief Patron, MREI
Dr. Prashant Bhalla, President, MREI
Dr. Amit Bhalla, Vice President, MREI
- Patron:** Prof. (Dr.) Sanjay Srivastava, VC, MRIIRS
- Co-Patron:** Prof. (Dr.) Pardeep Kumar, PVC/Dean FET/Dean FCA, MRIIRS
Prof. (Dr.) Naresh Grover, PVC & Dean Academics, MRIIRS
Shri R. K. Arora, Registrar, MRIIRS
- Organizers:**
Conference General Chair: Prof. (Dr.) S.S. Tyagi
Conference Chair: Prof. (Dr.) Supriya P. Panda, HoD, CSE, FET, MRIIRS
Conference CO-Chair: Prof. (Dr.) Tapas Kumar, HoD, CSE-Spl, FET, MRIIRS
Conference Convener: Prof. (Dr.) Suresh Kumar, CSE, FET, MRIIRS
Conference CO-Convener: Prof. (Dr.) Brijesh Kumar, CSE, FET, MRIIRS
Prof. (Dr.) Indu Kashyap, CSE, FET, MRIIRS
- Organizing Secretaries:** Dr. Poonam Tanwar, Associate Professor, CSE, FET, MRIIRS
Dr. Madhumita Kathuria, Associate Professor, CSE, FET, MRIIRS
- IEEE Delhi Section Oversight Committee (SOC):**
• Prof. Mahesh Bundeale, Lead-Member to Coordinate COM-IT-CON,2022 & Executive Committee Member-IEEE Delhi Section & Member-IEEE Delhi Section STDCOM on T & P Act.
• Prof. Sudhanshu Shekhar Jamar, Member-IEEE Delhi Section STDCOM on T & P Act.
• Dr. Rajnish Sharma, Secretary-IEEE Delhi Section & Member-IEEE Delhi Section STDCOM on T & P Act.
• Dr. Chakradhar C Reddy, Member-IEEE Delhi Section STDCOM on T & P Act.
• Dr. Pradeep K Gupta, Member-IEEE Delhi Section STDCOM on T & P Act.

Venue



Department of Computer Science & Engineering,
Faculty of Engineering & Technology,
Manav Rachna International Institute of Research
and Studies (MRIIRS),
(Formerly Manav Rachna International University,
(MRIU)),
Delhi Surajkund Road, Sector 43,
Faridabad, Haryana-121004.

Publication

All registered & presented papers will be considered for publishing in Conference Proceedings by IEEE XPLORE (IEEE Conference Record Number #54601) (Scopus Indexed)

Paper Submission

Authors must ensure that their works are produced in a way that conceals their identities. Submitted paper should be original research work, which has not submitted elsewhere (conference or journal) or and undergoing review. Submitted paper should follow the IEEE conference template and reference.
Link for abstract/paper submission: <https://easychair.org/conferences/?conf=comitcon2022>

Registration

At least one of the authors of the paper has to register for the conference. It is mandatory to present the paper in the conference for the inclusion of the paper in the conference proceedings.

	For Indian Authors and Delegates (in INR)		For Foreign Authors and Delegates (in USD)	
	IEEE Member	General	IEEE Member	General
Full-Time Students	4000	5000	160	230
Faculty & Research Scholars	6000	8000	130	170
Industry	8000	10,000	200	250
Attendee only	3000	5000	50	80

About Manav Rachna International Institute of Research and Studies (MRIIRS)

Manav Rachna International Institute of Research and Studies (MRIIRS), formerly MRIU, a Deemed-to-be-University under section 3 of the UGC Act, 1956, and a NAAC accredited 'A' Grade Institution, is a continuum of excellence from the Career Institute of Technology and Management (CITM). MRIIRS has been awarded a 5-Star QS rating for Teaching, Facilities & Social Responsibility. MRIIRS ranked among top institutions by Ministry of Education's NIRF Ranking 2021. It is a visible symbol of knowledge & experience providing high-quality education in the fields of Engineering & Technology, Health & Applied Sciences, Management, Computer Applications, Hotel Management, Commerce, Humanities, Media, Architecture, Design, Liberal Arts, Economics, Applied Psychology and many more. Manav Rachna International Institute of Research and Studies is a Founder Member of the prestigious "College Board's Indian Global Higher Education Alliance".

About Department of Computer Science & Engineering (CSE)

The Department of Computer Science & Engineering (CSE) was established in 1997. The department focuses on mastering the fundamental concepts both theoretically and practically. Keeping in view the growth in industry and increasing demand for computer professionals, we also offer some industry associated courses. The course curriculum has been specially tailored to fulfill the growing global outlook and focuses on upcoming technologies in the field of Computer Science and Engineering, to cater to the needs of the industry and R&D organizations. One should choose Computer Science and Engineering (CSE) as there are plenty of opportunities in this area, the platforms are incredibly diverse, and the jobs guarantee high remuneration with excellent job satisfaction. The B. Tech (CSE) programme offered by the Department has been accredited thrice by the National Board of Accreditation (NBA) in 2003, 2007, and 2016.

Important Dates

Last Date for Paper Submission:	22nd February 2022
Acceptance Notification Date:	28th February 2022
Camera Ready Manuscript Deadline:	1st March 2022
Conference Registration Deadline:	30th March 2022
Conference Dates:	26th-27th May 2022

Call for Papers

As observed in current status and future trends, the 4th industrial revolution is humanity's most recent advancement. The cornerstones of Industry 4.0 are Artificial Intelligence, Machine Learning, Big Data, Cloud Computing, and Parallel Computing. These pillars embrace the ability to make their own decisions through Machine Learning, as well as to collect data in real-time, to be analyzed and saved into the Cloud. They also allow for the use of pillars like the Internet of Things (IoT), Augmented Reality, Cyber Security, Cloud Computing, and finally 5G Networks.

Track 1: Artificial Intelligence & Machine Learning
Classification and Clustering, Deep Learning, Inductive Learning, Inference Rule Learning, Learning and Adaptive control, Real Time Event Learning and Detection, NLP, Statistical and Evolutionary Learning, Soft Computing, Image Processing, Computer Vision, IoT, Virtual Reality and Augmented Reality, Robotics, Annotation of Media Content.

Track 2: Cloud Computing & Virtualization
Cloud Management and Operations, Cloud Security, Cloud Computing in BI, Cloud Performance, Scalability and Reliability, Data Analytics in Cloud, Cloud Development and its Applications.

Track 3: Big Data Analytics & Visualization
Big Data Analytics and Applications, Big Data Search and Mining, Visualization Analytics for Big Data, Scalability and Efficiency, Critical Infrastructure Security.

Track 4: Semantic Web & Information Retrieval
Ontology-based Data Access, Information Retrieval and Semantic Analysis, Knowledge-Driven NLP, Novel Techniques for the Semantic Web, Multi-agent and Autonomous Systems, Ontology-based Neural Symbolic Reasoning, Blockchain-based Semantic Data Processing, Multi-modal Semantic Reasoning, Semantic Social Network Mining, Geospatial Semantics.

Track 5: Block Chain & Cyber Security
Bit coin and Cryptocurrency, Transaction and Security Awareness, Public Vs. Private Blockchain, Blockchain Application and Platforms, Decentralized Autonomous Solutions, Communication Network on Blockchain, Access and Identity Management, Integrity Issues and Ethical Hacking, Emerging Threats, Cyber Crime and its Solutions, Data Privacy.

Track 6: Data Science & Engineering
Data Handling and Pre-processing, Pattern Recognitions, Feature Engineering, Computational Biology & Bioinformatics, Data Modeling & Semantic Engineering, High Performance Commercial Applications, Infoscience & Computational Informatics, Information Network Analytics, Data Science Techniques Pandemic/Epidemic Management, Time Series and Sequential Pattern Mining.

Track 7: GPU & Parallel Computing
Multicore Systems, High Performance Systems, Heterogeneous Systems, Parallel Algorithms Performance Evaluation, Parallel Programming Languages, FPGAs and GPGPU, Parallel Environment Compilers, GPU Stream Processors, BDA and Parallel Computing, Efficient Resource Utilization, Parallel Architecture Operating Systems, Load Balancing, Parallel/Grid Computing, High Performance Computing, Real World Applications.

Contact us

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